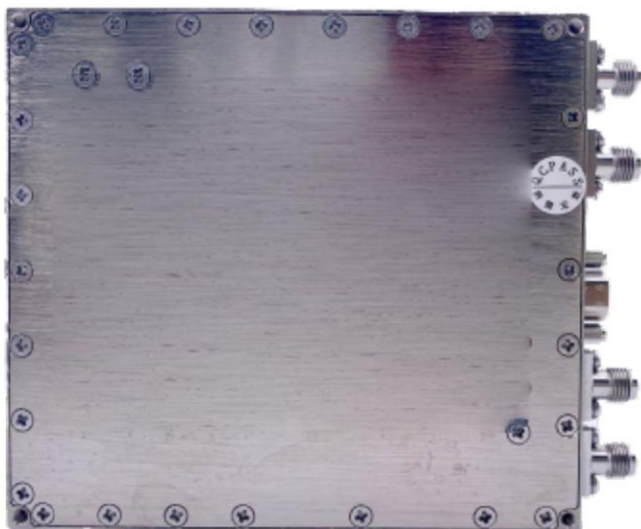


ULTRA-LOW PHASE NOISE FREQUENCY SYNTHESIZER



5 to 10 GHz

Ultra-low phase noise
SPI control
0.1 mHz frequency step
Switching time: ≤ 20 ms typical
100 × 90 × 18 mm package
Weight: ≤ 0.5 kg

The OTFS35Y-10L is a ultra-low phase noise frequency synthesizer with an output frequency range of 5 to 10 GHz. Control interface: SPI. Reference frequency: 10 MHz. The frequency step is 0.1 mHz. Frequency switching time: ≤ 20 ms typical. Output power: ≥ 9 dBm. Dimensions: 100 × 90 × 18 mm. Weight: ≤ 0.5 kg. Operating temperature: 0 to +50 °C.

TECHNICAL SPECIFICATIONS

OTFS35Y-10L

Property	Value
Output frequency range	5-10 GHz
Frequency step	0.1 mHz
Frequency switching time	≤20 ms typical
Output power	≥9 dBm
Frequency stability	±0.2 ppm (internal reference)
Spurious level	≤-90 dBc typical; ≤-85 dBc maximum
Harmonic level	≤-10 dBc
Reference frequency	10 MHz
Reference input power	7 ±3 dBm
Reference phase noise	≤-150 dBc/Hz at 100 Hz offset; ≤-158 dBc/Hz at 1 kHz offset; ≤-160 dBc/Hz at 10 kHz offset; ≤-160 dBc/Hz at 100 kHz offset; ≤-160 dBc/Hz at 1 MHz offset
Supply voltage	+12 V ±0.5 V; maximum +15 V
Supply current	≤2.2 A startup; 1.95 A steady state
Control interface	SPI
Operating temperature	0 to +50 °C
Storage temperature	-20 to +70 °C
Dimensions	100 × 90 × 18 mm
Weight	≤0.5 kg
Clock output	100 MHz; ≥10 dBm; spurious level ≤-80 dBc; harmonic level ≤-30 dBc
Clock phase noise	≤-135/-167/-178/-180/-180 dBc/Hz at 100 Hz offset/1 kHz/10 kHz/100 kHz/1 MHz offsets
Sweep input	YIG sweep voltage input (VT)
RF output connector	Removable SMA-K
Reference/clock/VT connectors	Removable SMA-K
Power/control connector	J30J-9-ZKP
Internal reference	Built-in low-phase-noise, high-stability crystal oscillator

TECHNICAL INFORMATION

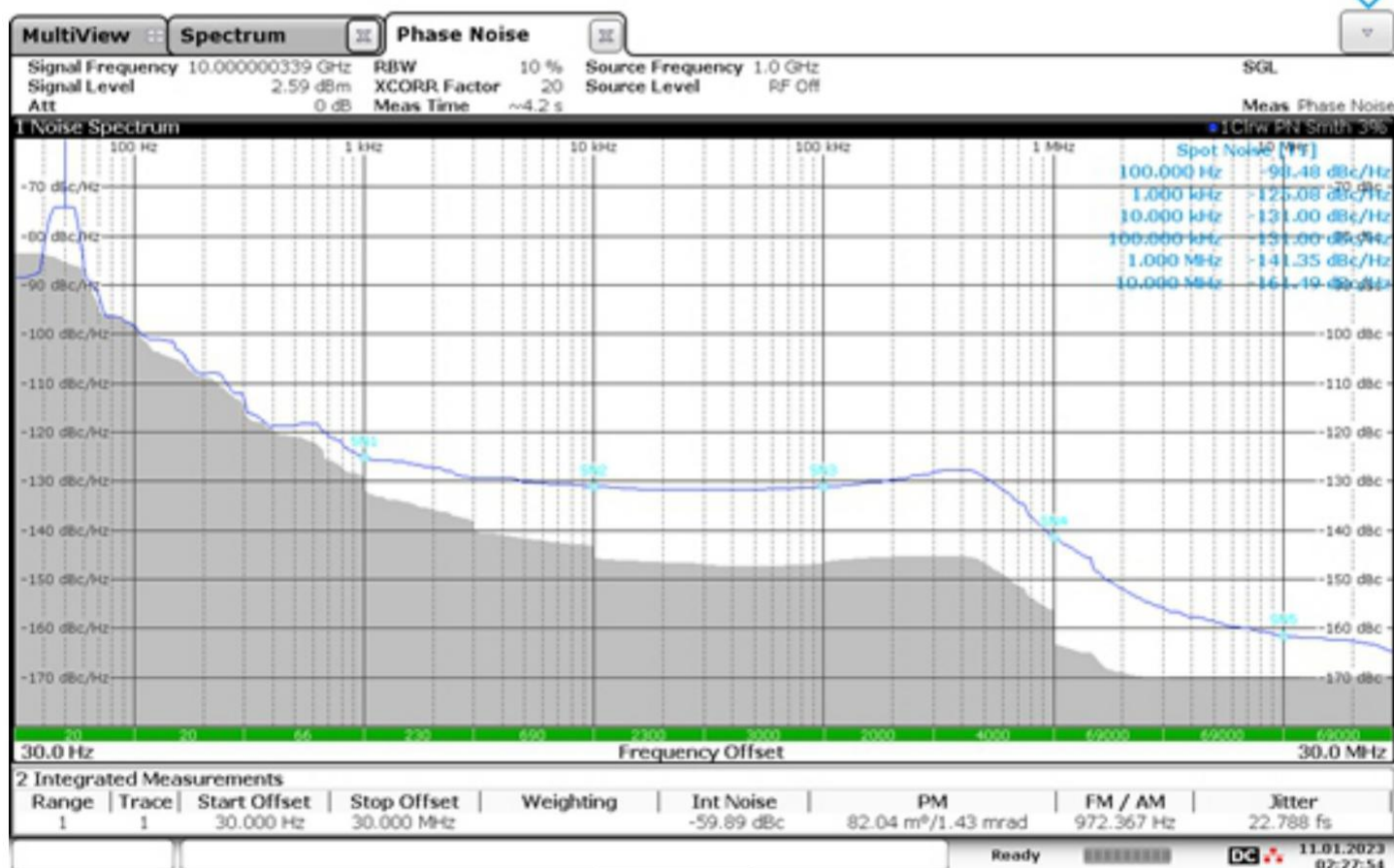
OTFS35Y-10L | Phase Noise (dBc/Hz)

Output Frequency	100 Hz	1 kHz	10 kHz	100 kHz	1 MHz
5 GHz	≤-101	≤-126	≤-135	≤-135	≤-144
10 GHz	≤-95	≤-122	≤-130	≤-130	≤-140

Column headings specify frequency offset from the carrier. Output frequency is listed in the first column. Values and limit signs follow the source tables.

10 GHz Phase Noise Measurement

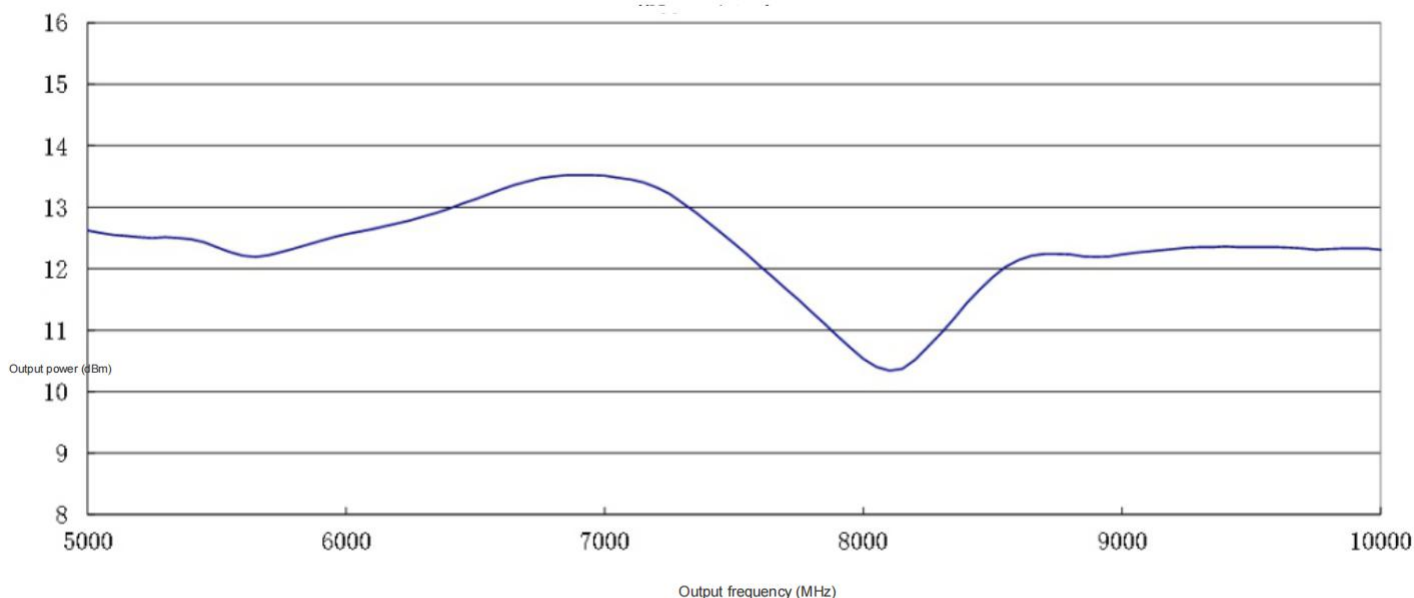
10GHz相位噪声曲线图



Source measurement example. Curves represent the illustrated test conditions.

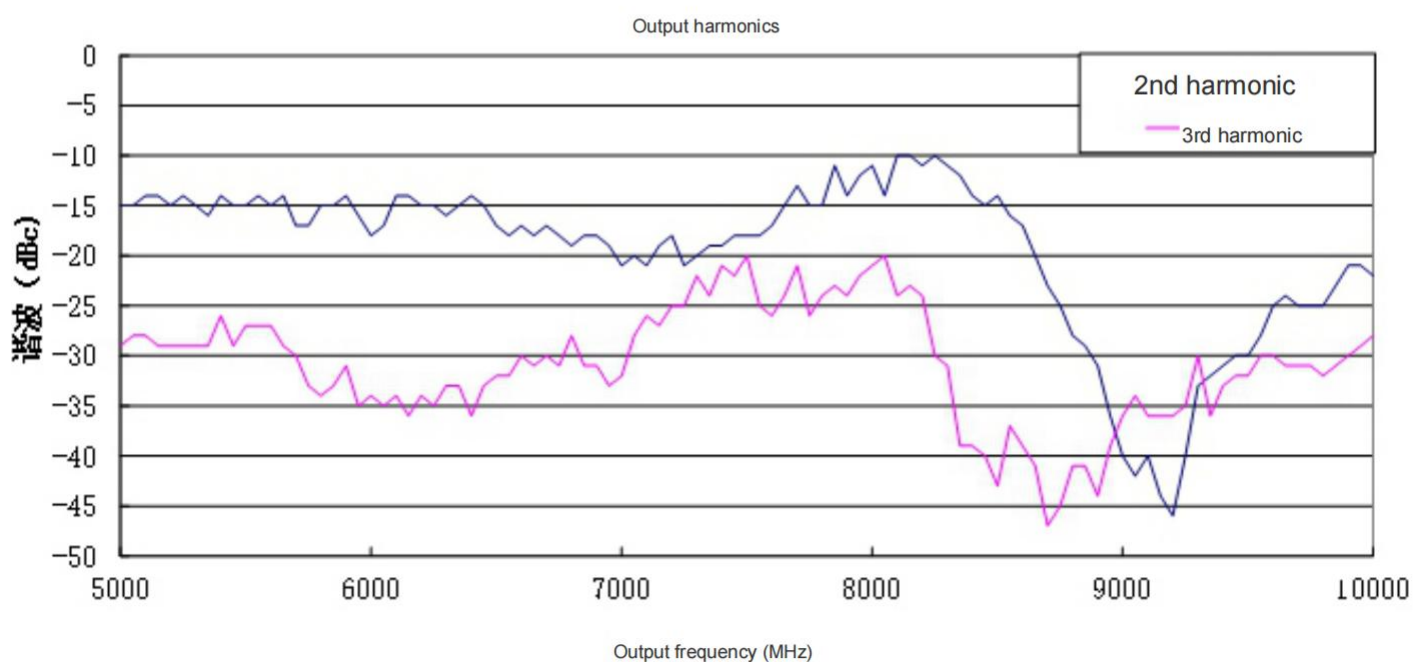
MEASUREMENT CURVES

Output Power vs. Frequency



Source measurement example. Curves represent the illustrated test conditions.

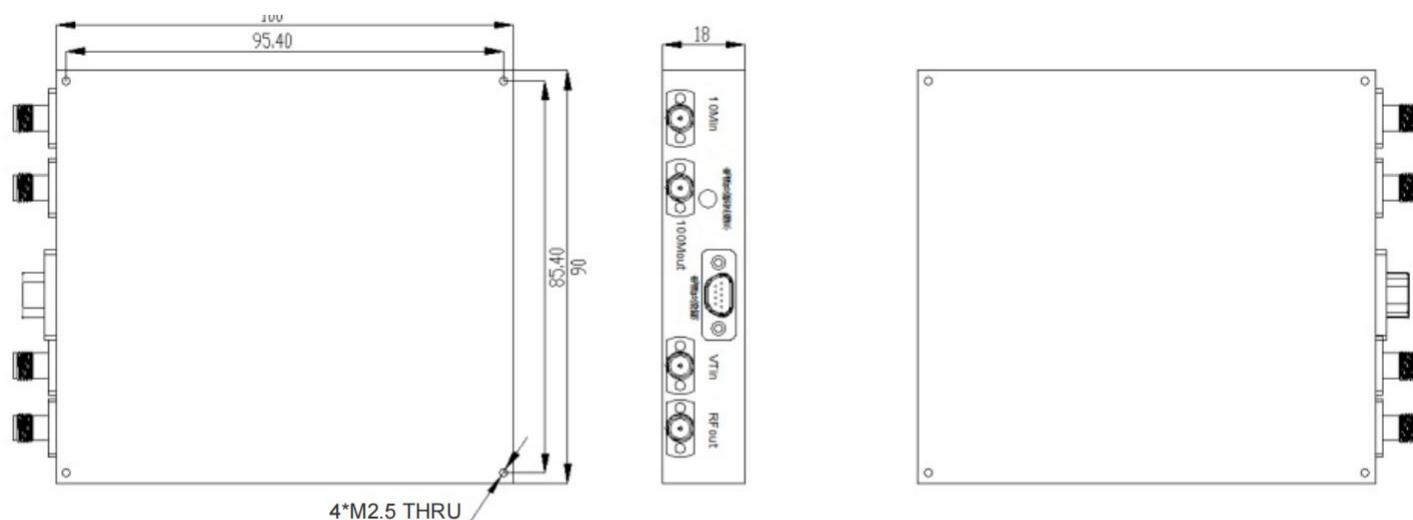
Harmonic Levels vs. Frequency



Source measurement example. Curves represent the illustrated test conditions.

TECHNICAL INFORMATION

Mounting Dimensions



Dimensions in mm.

Connector Definitions

Connector	Definition
10M in / 100M out / VT in	Removable SMA-K; pin diameter 0.38 mm
RF out	Removable SMA-K; pin diameter 0.38 mm
Power/control	J30J-9-ZKP

INTERFACES & PIN ASSIGNMENTS

J30J-9-ZKP Pin Assignments

Pin	Definition
1	+12 V supply
2	+12 V supply
3	GND
4	GND
5	LD: lock indication, high when locked
6	MOSI: SPI interface
7	MISO: SPI interface
8	SCK: SPI interface
9	LE: SPI interface